

CTX01-19603-R

Dual conductor high current power inductors



Product features

- Dual conductor, two-turn construction
- Magnetically shielded
- 15.1 mm x 8.6 mm footprint surface mount package in a 6.6 mm height
- Ferrite core material
- Moisture Sensitivity Level: 1

Applications

- Compatible with Picor® Cool-Power® ZVS Buck and Buck-Boost Regulator Families

Environmental data

- Storage temperature range (Component): -55 °C to +125 °C
- Operating temperature range: -55 °C to +125 °C (ambient plus self-temperature rise)
- Solder reflow temperature: J-STD-020 (latest revision) compliant
- Halogen free, lead free, RoHS compliant



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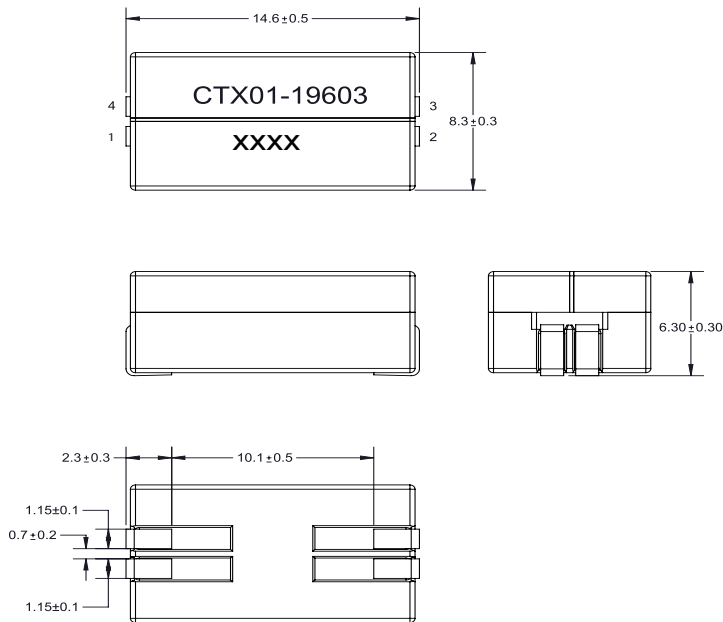
Product specifications

Part Number ⁶	OCL ¹ (nH) +6%/-5%	FLL ² (nH) minimum	I _{rms} ³ (A)	I _{sat} 1 ⁴ (A)	DCR ⁵ (mΩ) @ +20 °C
CTX01-19603-R	375	349	16	52	1.15 ±0.173

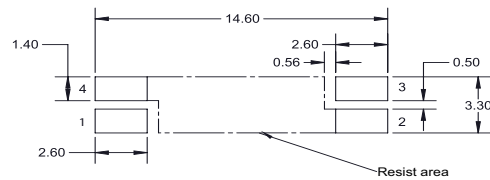
1. Open Circuit Inductance (OCL) Test Parameters: 1 MHz, 0.1 Vrms, 0.0 Adc, +25 °C
2. Full Load Inductance (FLL) Test Parameters: 1 MHz, 0.1 Vrms, I_{sat}1, +25 °C
3. I_{rms}: DC current for an approximate temperature rise of 40 °C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed +125 °C under worst case operating conditions verified in the end application.

4. I_{sat}1: Peak current for approximately 2% rolloff @ +25 °C
5. DCR measured from Pins (1-2) and (4-3)
6. Part Number Definition: CTX01-19603-R
CTX01-19603= Product code
-R suffix = RoHS compliant
Note: Hipot: 200 Vdc minimum for 2 seconds, 0.1 mA pins (1-2) to (4-3)

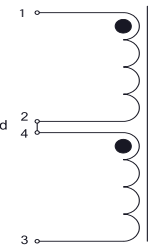
Dimensions (mm)



Recommended Pad Layout



Schematic

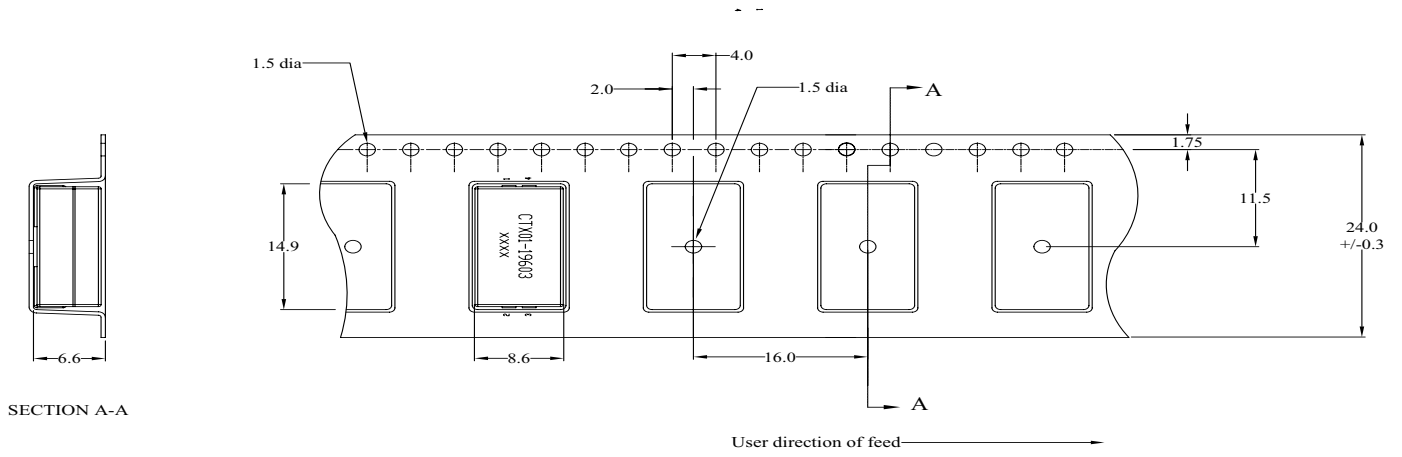


NOTE:
Pins 2 & 4 are connected
thru the PCB trace

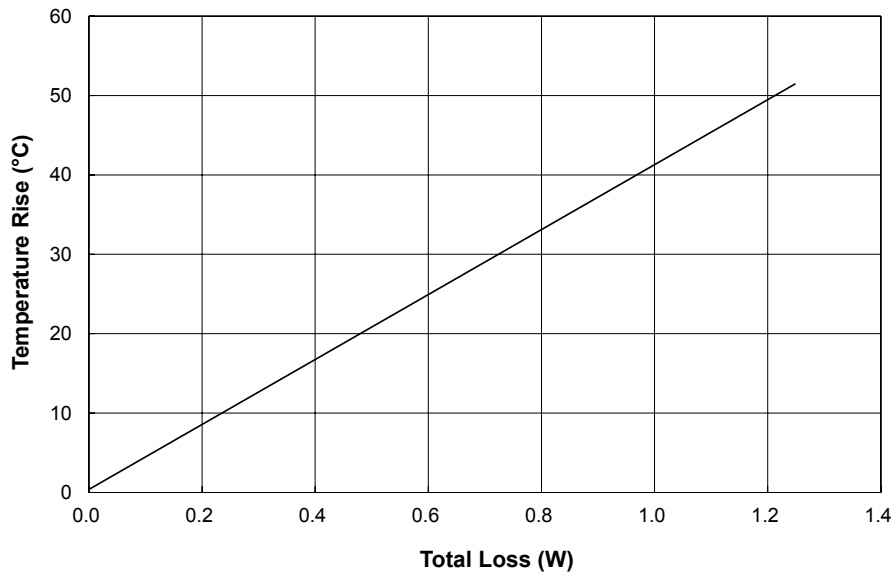
Part marking: CTX01-19603,
xxxx=lot code
All soldering surface to be coplanar within 0.1 millimeters
Pad layout tolerances are ±0.1 millimeters unless stated otherwise
Pins 2 and 4 are connected through the PCB trace
Do not route traces or vias underneath the inductor

Packaging information (mm)

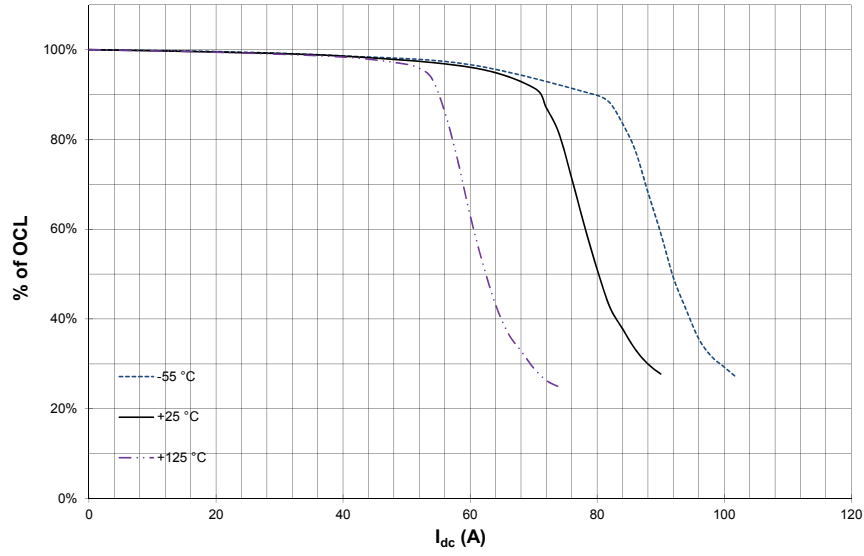
Supplied in tape and reel packaging , 600 parts per 13" diameter reel



Temperature rise vs. total loss



Inductance characteristics



Solder reflow profile

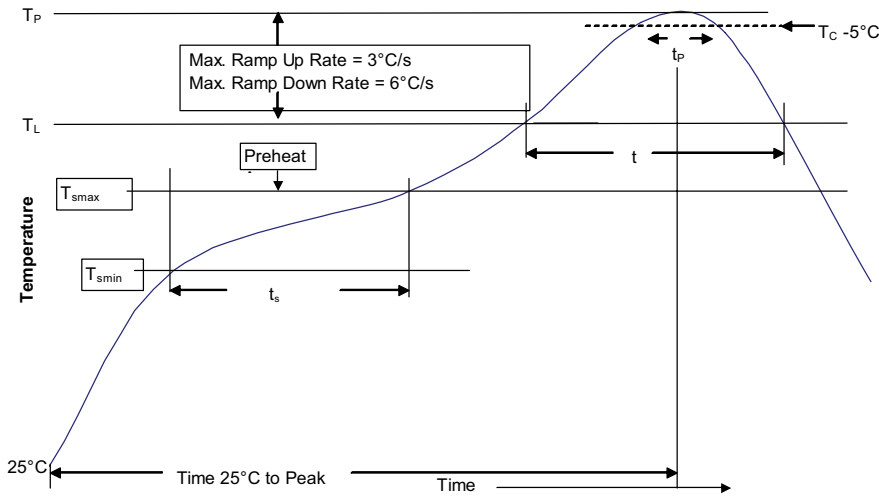


Table 1 - Standard SnPb Solder (T_C)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5mm)	235 °C	220 °C
≥2.5mm	220 °C	220 °C

Table 2 - Lead (Pb) Free Solder (T_C)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350 - 2000	Volume mm ³ >2000
<1.6mm	260 °C	260 °C	260 °C
1.6 – 2.5mm	260 °C	250 °C	245 °C
>2.5mm	250 °C	245 °C	245 °C

Reference JDEC J-STD-020

Profile Feature	Standard SnPb Solder	Lead (Pb) Free Solder
Preheat and Soak		
• Temperature min. (T _{smin})	100 °C	150 °C
• Temperature max. (T _{smax})	150 °C	200 °C
• Time (T _{smin} to T _{smax}) (t _s)	60-120 Seconds	60-120 Seconds
Average ramp up rate T _{smax} to T _p	3°C/ Second Max.	3 °C/ Second Max.
Liquidous temperature (T _L)	183 °C	217 °C
Time at liquidous (t _L)	60-150 Seconds	60-150 Seconds
Peak package body temperature (T _p)*	Table 1	Table 2
Time (t _p)** within 5 °C of the specified classification temperature (T _C)	20 Seconds**	30 Seconds**
Average ramp-down rate (T _p to T _{smax})	6 °C/ Second Max.	6 °C/ Second Max.
Time 25 °C to Peak Temperature	6 Minutes Max.	8 Minutes Max.

* Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.
 ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

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Eaton
Electronics Division
 1000 Eaton Boulevard
 Cleveland, OH 44122
 United States
www.eaton.com/electronics

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